ABSTRACT

The present invention relates to a method for the synchronization of a first and a least one second module, each having a clock generator. The invention furthermore relates to such modules, a master program module, a slave program module and a device for this purpose. It is proposed that the first module, transmits a first clock signal generated by its clock generator to the second module, which synchronizes its clock generator with the first clock signal. The second module transmits a second clock signal generated by its clock generator that is synchronized with the first clock signal to the first module, which determines a time difference value between the first clock signal and the second clock signal, which time difference value is essentially due the transmission time of the first and the second clock signal between the first and the second module. The first module transmits an item of information about the (first) time difference value to the second module, which adjusts its clock generator on the basis of said information.